ACADEMIC PLANNER

B.Sc.(H) I SEM
Computer System Architecture

Teacher: Dr. Shalini Sharma

CHAPTERS	UNIT	COMPLETION MONTH	ASSESSMENT
Ch 1: Digital Logic Circuits	UNIT 1	September	• Assignment Unit-1 • Quiz/Problem Solving exercise in class
Ch 2: Digital Components	UNIT 2	Septemer	• Test ch-1 • Assignment Unit-2 • Quiz/Problem Solving exercise in class
Ch 4: Register Transfer and Micro-operations Ch 3: Data Representation	UNIT 3	October	• Test ch- 2 &3 • Assignments/quiz Unit-3 • Quiz/Problem Solving exercise in class
Ch 5: Basic Computer Organization and Design Ch 9: Pipeline and Vector Processing	UNIT 4	October + November	• Test ch-4 &5 • Assignments Unit-4 • Quiz/Problem Solving exercise in class
Ch 8: Central Processing Unit Ch-7: Multicores, Multiprocessors, and Clusters	UNIT 5	November	• Test Ch- 7,8 & 9 • Assignments Unit-5 • Quiz/Problem solving exercise in class.
Ch 11: Input Output Organization Ch 12: Memory Organization	UNIT 6	December	• Test unit 6 • Assignment Unit-6 • Mock practical & viva